

ABSTRACT OF THE DISCLOSURE

A tri-state detection circuit includes a first input port for receiving a tri-state input signal, a clock input port for receiving a clocking signal, a first output port, a second output port coupled to the first input port, a D-flip-flop and a buffer. The D-flip-flop has a D input, a clock input CLK, and a Q output. The D input is tied high.

5 The clock input CLK is coupled to the first input port. The Q output is coupled to the first output port. The buffer has a buffer input and a buffer output. The buffer input is coupled to the clock input port. The buffer output is coupled to the clock input CLK of the D-flip-flop.